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## APPLICANTS

Hiroyuki Hasegawa, Tokyo, JAPAN;

Tomonori Yamaoka, Tokyo, JAPAN;

Yoshio Ishihara, Tokyo, JAPAN; Hiroshi Masusaki, Tokyo, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/254,601 09/26/2002 PAT 6,794,204  
 which is a DIV of 09/793,124 02/27/2001 PAT 6,776,805

Yes

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-052519 02/28/2000

JAPAN 2000-108563 04/10/2000

Yes

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/30/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: <i>Mark A. Stevenson</i> Initials: <i>MS</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
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## ADDRESS

00909  
 PILLSBURY WINTHROP SHAW PITTMAN, LLP  
 P.O. BOX 10500  
 MCLEAN, VA  
 22102

## TITLE

Semiconductor manufacturing method and semiconductor manufacturing apparatus

FILING FEE

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